

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of)	
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Hiroshi KOMURASAKI et al.)	Group Art Unit: Unassigned
)	
Application No.: Unassigned)	Examiner: Unassigned
)	
Filed: August 21, 2003)	Confirmation No.: Unassigned
)	
For: OSCILLATOR CIRCUIT AND L)	
LOAD DIFFERENTIAL CIRCUIT)	
ACHIEVING A WIDE)	
OSCILLATION FREQUENCY)	
RANGE AND LOW PHASE NOISE)	
CHARACTERISTICS)	

**FIRST
INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure as set forth in 37 C.F.R. § 1.56, Applicants hereby submit the following information in conformance with 37 C.F.R. §§ 1.97 and 1.98. Pursuant to 37 C.F.R. § 1.98, a copy of each of the documents cited is enclosed.

Japanese Patent Nos. 7-142258, 8-162331, and both publications are discussed in the specification.

The documents are being submitted within three (3) months of the filing or entry of the national stage of this application or before the first Office Action on the merits, whichever is later. Since document is being filed within the time period set forth in 37 C.F.R. § 1.97(b) no fee or statement is required.

To assist the Examiner, the documents are listed on the attached form PTO-1449. It is respectfully requested that an Examiner-initialed copy of this form be returned to the undersigned.

Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS, L.L.P.

Date: August 21, 2003

By: 

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Substitute for forms 1449A/PTO & 1449B/PTO	ATTORNEY'S DKT No.	APPLICATION No.
	009683-477	Unassigned
	APPLICANT	
	Hiroshi KOMURASAKI et al.	
FIRST INFORMATION DISCLOSURE STATEMENT BY APPLICANT	FILING DATE	GROUP
	August 21, 2003	Unassigned

U.S. PATENT DOCUMENTS				
Examiner Initials	Document Number	Kind Code (if known)	Name of Patentee or Applicant of Cited Document	Issue/Publication Date (MM-DD-YYYY)

FOREIGN PATENT DOCUMENTS						
Examiner Initials	Document Number	Kind Code (if known)	Country	Date of Publication (MM-DD-YYYY)	Translation Yes No	
	*7-142258		Japan	06-02-1995	Abst	
	*8-162331		Japan	06-21-1996	Abst	

NON PATENT LITERATURE DOCUMENTS	
Examiner Initials	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
	*Akihiro YAMAGISHI et al., "A LOW-VOLTAGE 6-GHz-BAND CMOS MONOLITHIC LC-TANK VCO USING A TUNING-RANGE SWITCHING TECHNIQUE," IEICE Trans. Fundamentals, Vol. E84-A, No. 2, February 2001, pp. 559-562.
	*T. OHNAKADO et al., "A 1.4dB INSERTION-LOSS, 5GHz TRANSMIT/RECEIVE SWITCH UTILIZING NOVEL DEPLETION-LAYER-EXTENDED TRANSISTORS (DETs) IN 0.18 μ m CMOS PROCESS," IEEE Symposium on VLSI Technology Digest of Technical Papers, June 2002, 16.4, pp. 162-163.

Examiner Signature		Date Considered	
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with M.P.E.P. § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. **SEND TO:** Assistant Commissioner for Patents, Washington, DC 20231.

***Also discussed in the specification.**